



George

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Attorney Docket No: 042390.P12093

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Takashi Kumamoto, et al.

Application No.: 09/893,588

Filing Date: June 29, 2001

For: Multi-Purpose Planarizing/Back-grind/Pre-
Underfill Arrangements for Bumped Wafers and
Dies

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Examiner: A. Chambliss

Art Unit: 2827

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Date

3/31/04

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The assignee, Intel Corporation, of the above-identified Patent Application, hereby revokes all previous powers of attorney given in this Patent Application, and appoints the firm identified below and individual.

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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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Date: March 30, 2004

By: Pam Matlock
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